

Download Multithreading Architecture Dean Tullsen

Dean M. Tullsen. I am a professor and chair of the Department of Computer Science and Engineering at the University of California, San Diego. I do research in the area of Computer Architecture -- for more information, see my research interests and publications page. I will be teaching CSE 240A in Fall 2017 and CSE 148 in Spring 2018. An instruction set architecture (ISA) is an abstract model of a computer. It is also referred to as architecture or computer architecture. A realization of an ISA is called an implementation. An ISA permits multiple implementations that may vary in performance, physical size, and monetary cost (among other things); because the ISA serves as the interface between software and hardware. International Journal of Engineering Research and Applications (IJERA) is an open access online peer reviewed international journal that publishes research ..